

**LIST OF REFERENCES CITED BY  
APPLICANT & TRADEMARK OWNER**  
*(use as many sheets as necessary)*

Application Number: 09/911,038  
 Filing Date: July 23, 2001  
 First Named Inventor: Eiji OKI  
 Group Art Unit: Not yet assigned  
 Examiner Name: Not yet assigned

Sheet

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of

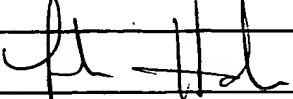
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Attorney Docket No.: Poly-22-2/APP

**OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume, issue number(s), publisher, country, where published, source	T <sup>2</sup>
ftt	AA.	N. W. McKeown, "Scheduling Algorithms for Input-Queued Cell Switches," PhD Thesis, University of California at Berkeley, (1995).	
ftt	AB.	C. Y. Lee and A. Y. Oruç, "A Fast Parallel Algorithm for Routing Unicast Assignments in Benes Networks," IEEE Trans. on Parallel and Distributed Sys., Vol. 6, No. 3, pp. 329-334 (March 1995).	
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ftt	AE.	T. Chaney, J. A. Fingerhut, M. Flucke, J. S. Turner, "Design of a Gigabit ATM Switch," Proc. IEEE INFOCOM '97, pp. 2-11 (April 1997).	
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ftt	AL.	N. McKeown, A. Mekkittikul, V. Anantharam, and J. Walrand, "Achieving 100% Throughput in an Input-Queued Switch," IEEE Trans. on Communications, Vol. 47, No. 8, pp. 1260-1267 (Aug. 1999).	
ftt	AM.	E. Oki, N. Yamanaka, Y. Ohtomo, K. Okazaki and R. Kawano, "A 10-Gb/s (1.25 Gb/s x 8) 4 x 2 0.25-μm CMOS/SIMOX ATM Switch Based on Scalable Distributed Arbitration," IEEE J. of Solid-State Circuits, Vol. 34, No. 12, pp. 1921-1934 (Dec. 1999).	
ftt	AN.	N. Yamanaka, E. Oki, S. Yasukawa, R. Kawano and K. Okazaki, "OPTIMA: Scalable, Multi-Stage, 640-Gbit/s ATM Switching System Based on Advanced Electronic and Optical WDM Technologies," IEICE Trans. Commun., Vol. E83-B, No. 7, pp. 1488-1496 (July 2000).	
ftt	AO.	J. Chao, "Saturn: A Terabit Packet Switch Using Dual Round-Robin," IEEE Communications Magazine, pp. 78-84 (Dec. 2000).	
ftt	AP.	G. Nong and M. Hamdi, "On the Provision of Quality-of-Service Guarantees for Input	

<del>ft</del>	Queued Switches," IEEE Commun. Mag., pp. 62-69 (Dec. 2000).	
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<del>ft</del> AS.	A. Smiljanic, "Flexible Bandwidth Allocation in Terabit Packet Switches," pp. 233-239.	

Examiner Signature		Date Considered	08/01/2005
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Unique citation designation number. 2 Applicant is to place a check mark here if English language translation is attached.

